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Averyt et al.

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(54) **PULSE-WIDTH MODULATION AND ARBITRATION FOR CONTEXTUAL AND UNIFORM LED ILLUMINATION IN USB APPLICATIONS**

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(58) **Field of Classification Search**
CPC H05B 45/18; H05B 45/325; B60Q 3/18; G06F 3/162; G06F 13/385
See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

6,363,491 B1 3/2002 Endo 713/310
7,689,130 B2* 3/2010 Ashdown H04B 10/116
398/118

(Continued)

FOREIGN PATENT DOCUMENTS

CN 107172739 A 9/2017 H05B 44/00
EP 3291098 A1 3/2018

OTHER PUBLICATIONS

International Search Report and Written Opinion, Application No. PCT/US2020/050040, 12 pages, dated Dec. 22, 2020.

(Continued)

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(57) **ABSTRACT**

A PWM lighting bridge manages hardware PWM compensation components. One or more PWM outputs are driven based upon a master PWM input signal. The master PWM input signal is processed through a mapping function to produce a desired output signal(s) on the one or more PWM outputs. The mapping function comprises a set of compensation bins having a maxim input duty cycle, a minimum input duty cycle and an output duty cycle. The mapping process selects a bin based on determining whether the input duty cycle is within the minimum and maximum duty cycle ranges of the bins and then uses the specified output duty cycle for the one or more PWM outputs. The PWM lighting bridge provides configuration and runtime management of the hardware PWM compensation components. Read/write access to the PWM compensation bins provides customizable configurations of the input and output PWM characteristics.

21 Claims, 5 Drawing Sheets

